

Amendments to the Specification

Please replace the paragraph bridging pages 5 and 6 with the following paragraph:

A1
Furthermore, even for the same data modulation/demodulation formula, parameters with respect to the receiving and transmission are changed based on the change of a ~~career~~ carrier frequency and a modulation rate although the algorithm is unchanged. In the above state, it is necessary to change all programs in compliance with the change of the ~~career~~ carrier frequency and the modulation rate, because the internal program RAM is initialized by the reset signal and it is impossible to change a part of the program. Otherwise, it is necessary to load every parameter to the program RAM with the same modulation form.

Please replace the paragraph at lines 9-20 of page 11 with the following paragraph:

A2
It is also object to provide a modem, wherein the clock signal control part includes a forward circuit for forwarding a desired part of the programs read from the external memory part to the internal memory. Therefore, in case of that parameters only related to transmitting and receiving are different in a state where a ~~career~~ carrier frequency and a modulation rate are different even if modular formulation is same, it is possible to keep the modulation formulation program and change only parameters, because it is possible to change not all but a part of the modulation/demodulation program.

Please replace the paragraph bridging pages 12 and 13 with the

following paragraph:

A3 Referring to Fig. 1, a DSP signal processing apparatus 1 includes a DSP 2, an external ROM 4, and a forward circuit 5. The DSP 2 has an internal program RAM 3 in which a program to be executed is stored. The external ROM 4 stores all executable programs by the DSP 2. The forwarding circuit 5 forwards a desired program from the external ROM 4 to the internal program RAM 3. Besides, the DSP signal processing apparatus 1 includes a clock generating circuit 6 and a clock control circuit 7. The clock generating circuit 6 generates a clock signal CLK, which is supplied to the DSP 2 and the forwarding circuit 5. The clock control circuit 7 controls outputting of the clock signal CLK generated by the clock generating circuit 6 to the DSP 2. The forwarding circuit 5 and the clock control circuit 7 compose a clock signal control part 9.

Please replace the paragraph bridging pages 22 and 23 with the following paragraph:

A4 Thus, it is not necessary to reset an inside parameter of the DSP 2 and the parameter of the AFE 22 and the like after the program in the internal program RAM 3 is replaced, so that the modulation/demodulation program can be carried out immediately, by using the DSP signal processing apparatus 1 according to the above embodiment to the modem.

Therefore, as compared with the modem of prior arts, it is possible to reduce a possibility in that the communication is cut in case of that the program is replaced. Besides, in case of that parameters only related to transmitting and receiving are different in a state where a ~~carrier~~ carrier frequency and a modulation rate are different even if

Yasutoshi HIRANO, S.N. 09/955,885
Page 4

Dkt. No. 2271/65888

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formulation program and change only parameters, because it is possible
to change not all but a part of the modulation/demodulation program.
